Washington State University
MAJOR CURRICULAR CHANGE FORM - - NEW/RESTORE COURSE

☐ Please attach rationale for your request, a complete syllabus, and explain how this impacts other units in Pullman and other campuses (if applicable).
☐ Obtain all required signatures with dates.
☐ Provide original stapled packet of signed form/rationale statement/syllabus PLUS 10 stapled copies of complete packet to the Registrar's Office, campus mail code 1035.
☐ Submit one electronic copy of complete packet to wsu.curriculum@wsu.edu.

Requested Future Effective Date: Fall 2016 (term/year) Course Typically Offered: Spring

DEADLINES: For fall term effective date: October 1st; for spring or summer term effective date: February 1st. See instructions.
NOTE: Items received after deadlines may be put to the back of the line or forwarded to the following year. Please submit on time.

☐ New Course  ☐ Temporary Course  ☐ Restore Course

<table>
<thead>
<tr>
<th>ECE</th>
<th>501</th>
<th>Fundamentals of Laboratory-on-Chip</th>
</tr>
</thead>
<tbody>
<tr>
<td>course subject/crosslist</td>
<td>course no.</td>
<td>title</td>
</tr>
<tr>
<td>(3</td>
<td></td>
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</tr>
</tbody>
</table>

Credit hrs lecture hrs lab or studio hrs per week hrs per week
None

prerequisite

Description for catalog: Review of LoC Technologies, Operating Principles, Design Basics

Fabrication, integration with micro/nanoelectronic devices, sensor, power systems

Additional Attributes: Check all that apply.

☐ Crosslisting (between WSU departments)*

☐ Variable credit:

☐ Conjoint listing (400/500):

☐ Repeat credit (cum. max. hrs):

Special Grading: ☐ S, F; ☐ A, S, F (PEACT only); ☐ S, M, F (VET MED only); ☐ H, S, F (PHARMACY, PHARDSCI only)

☐ Cooperative with UI

☐ Other (please list request):

The following items require prior submission to other committees/depts. (SEE INSTRUCTIONS.)

☐ Request to meet Writing in the Major [M] requirement (Must have All-University Writing Committee Approval.)

☐ Request to meet UCORE in ____________________ (Must have UCORE Committee Approval >> See instructions.)

☐ Special Course Fee ____________________ (Must submit request to University Receivables.)

Contact: Praveen Sekhar Phone number: (360) 546-9186 Campus mail code: VECS
Email: praveen.sekhar@wsu.edu Instructor, if different:

Chair/date  Dean/date  All-University Writing Com / date

Chair (if crosslisted/interdisciplinary)*  Dean (if crosslisted/interdisciplinary)*  UCORE Committee Approval Date

Catalog Subcommittee Approval Date  GSC or AAC Approval Date  Faculty Senate Approval Date

*If the proposed change impacts or involves collaboration with other units, use the additional signature lines provided for each impacted unit and college.
Justification: ECE 501 Fundamentals of Lab-on-Chip

The focus of the proposed Master’s program in electrical engineering is Lab-on-Chip (LoC). Lab-on-a-chip (LoC) integrates several laboratory functions on a single chip of only millimeters in size. This course serves as an introductory and foundational course for students who like to pursue their thesis in LoC and serves as an overview course for students who wish to take a non-LoC option.

It does not affect other units in Pullman and other campuses.
ECE 501 Fundamentals of Laboratory-on-Chip
Spring 2015

Description: Operating principles of laboratory-on-chip (LoC) technologies, basics of design and fabrication, integration with microdevices, digital and high frequency circuits, sensors, and power systems.

Credits: 3

Prerequisites by course: None

Prerequisites by Topic: None


Recommended Text: Research papers uploaded by the instructor

Supplementary Materials: Will be posted on Blackboard.

Instructor: Dr. Praveen Sekhar
Office: VECS 201 W
Phone: (360) 546 9186
Email: praveen.sekhar@vancouver.wsu.edu
Office hours: Open Door Policy (E-mail Appointments Preferred)
Lecture Times and Location: VECS 104, MW 9:00-10:15 Am

Graduate Learning Outcomes (GLO)

Students will be able to:

GLO-1: Utilize the in-depth knowledge of LoC to design and fabricate devices for specific applications.

GLO-2: Layout a research problem in LoC topic, execute a research plan, including generating and analyzing research results
Learning Outcomes and Assessment

<table>
<thead>
<tr>
<th>Student Learning Outcomes for this Course:</th>
<th>Course Topics/Dates:</th>
<th>Evaluation of Outcome:</th>
</tr>
</thead>
<tbody>
<tr>
<td>At the end of the course, the students should be able to:</td>
<td>The following date(s) will address this outcome:</td>
<td>This outcome will be primarily evaluated by:</td>
</tr>
<tr>
<td>Understand the principles of Lab-on-Chip with emphasis on soft lithography and materials choices for fabrication</td>
<td>Week 1 (operating principles) and Week 5, 6, 7 (Fabrication)</td>
<td>Quiz, Homework, Tests</td>
</tr>
<tr>
<td>Familiarize with different Lab-on-Chip applications and understand the integration of different systems</td>
<td>Week 8, 9, 10,11 (selection of sensors, integration with digital and power systems)</td>
<td>Quiz, Homework, Tests</td>
</tr>
<tr>
<td>Prepare to layout a research problem based on Lab-on-Chip concept and identify solutions</td>
<td>Week 12, 13, and 14 (Case study 1, 2, 3 and 4)</td>
<td>Homework</td>
</tr>
<tr>
<td>Design and fabricate Lab-on-Chip devices</td>
<td>Week 1 to Week 14</td>
<td>Homework and Tests</td>
</tr>
</tbody>
</table>

Grading Policy

Homework 30%
Quiz 10%
Midterm Exam 30%
Final Exam 30%

A 94-100  
A- 90-93  
B 83-86  
B- 80-82  
C 73-76  
C- 70-72  
D 60-66  
D+ 67-69  
F < 59

Composition of final grade: Your course grade maps to the WSU learning outcomes as: 50% GLO-1 and 50% GLO-2.

Descriptions of Required Assignments
Majority of the homework assignments stem from the exercises in the required textbooks. The assignments are due exactly one week after it has been assigned.

Instructional Methods
The instructional methods include instructor lectures, video demonstrations, and student lead discussions.
Instructor Specific Expectations

1. Work cooperatively and effectively with others in class and on group assignments.
2. Participate fully in class activities and discussions.
3. Prepare thoroughly for class sessions by doing the necessary readings. All reading assignments will be posted on the course website.
4. Treat the instructor with courtesy and respect and maintain decorum in the class.
5. Carefully read the syllabus that is passed out on the first day of class and follow all instructions included in the syllabus.
6. Expect students to let instructor know if they are having problems understanding or working with the material.
7. Expect students to submit assignments on time and according to instructions.
8. Avoid the use of cell phones or laptops in class.
9. Understand the concepts to solve problems apart from textbook exercises.

Attendance Policy

Since majority of the course content is presented in a problem-solving format, daily attendance is preferred. Lecture attendance is not mandatory. Each class utilizes tools and concepts learned from previous classes. Please be sure to arrive on time and stay till the end of the class. Not only do excessive absences, tardiness, and early departure suggest a lack of professionalism and commitment, but they also guarantee that you will not attain the objectives of this course.

Website

All course materials (lecture notes, assignments) will be available on the Blackboard website at learn.wsu.edu

Make-up Exam/Assignment Policy

No make-up exam, assignments or quizzes will be given unless a medical or other emergency was the reason for missing the exam or the assignment. For any other reason you must first contact the instructor before missing an exam, a quiz or an assignment.

Late Homework Submission Policy

Late homeworks will not be entertained unless dire circumstances warrant it. Without a valid reason, there will be a 10% deduction grade for submitting late by a day. If the submission is two days late, a 20% deduction in grade will be enforced. The homeworks will not be accepted after three days of original submission.

WSU Academic Integrity Statement

Academic integrity is the cornerstone of the university and will be strongly enforced in this course. Any student found in violation of the Academic Dishonesty Policy, which can be found at http://studentaffairs.vancouver.wsu.edu/student-affairs/academic-dishonesty, will be given an “F” for the course and will be referred to the Office of Student Conduct. For additional information about WSU’s academic integrity policy/procedures, please contact (360) 546 9573.
WSUV Reasonable Accommodation Statement
Accommodations may be available if you need them in order to fully participate in this class because of a disability. Accommodations may take some time to implement so it is critical that you contact Disability Services as soon as possible. All accommodations must be approved through Disability Services, located in the Student Resource Center on the Lower Level of Student Services Center (360) 546-9138.

Emergency Notification System
- Review the Campus Safety Plan (http://www.vancouver.wsu.edu/safety-plan) and visit the WSU Vancouver Police website (www.vancouver.wsu.edu/police) for a comprehensive listing of University policies, procedures, statistics, and information related to campus safety, emergency management, and the health and welfare of the campus community.
- Everyone should also become familiar with the WSU VANCouG ALERT website (www.vancouver.wsu.edu/alerts) where information about emergencies and other issues affecting WSU will be found. This site also provides information on the communication resources WSU will use to provide warning and notification during emergencies. It should be bookmarked on computers.
- Finally, all faculty, staff, and students should go to the zzusis portal at http://zzusis.wsu.edu and register their emergency contact information for the Crisis Communication System (Mass Notification System). Enter your network ID and password and you will be taken to the zzusis portal page. Look for the Emergency Information box on the left side of the page and click on Update Now to be taken to the registration page where you can enter your cell, landline, and email contact information as well as arrange for emergency text messages to be sent to your cell phone.
- Finally, in case of class cancellation campus-wide, please check local media, the WSU Vancouver web page and/or http://www.flashalert.net/. Individual class cancellations may be made at the discretion of the instructor. Each individual is expected to make the best decision for his or her personal circumstances, taking safety into account. Please refer to the WSU Vancouver safety plan website for more information (http://www.vancouver.wsu.edu/safety-plan).

Week to week Course Outline
The tentative weekly schedule is listed below.

<table>
<thead>
<tr>
<th>Week</th>
<th>Date</th>
<th>Topic</th>
<th>HW Due Dates</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>TBD</td>
<td>TBD Operating principle: Laboratory-on-Chip</td>
<td></td>
</tr>
<tr>
<td></td>
<td>TBD</td>
<td>TBD Parts and components of LoC</td>
<td></td>
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<tr>
<td>2</td>
<td>TBD</td>
<td>TBD Applications of LoC</td>
<td>HW 1</td>
</tr>
<tr>
<td></td>
<td>TBD</td>
<td>TBD CMOS based LoC</td>
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<tr>
<td>3</td>
<td>TBD</td>
<td>TBD PCB LoC</td>
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<tr>
<td></td>
<td>TBD</td>
<td>TBD Electric field based LoC</td>
<td>HW 2</td>
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<tr>
<td>4</td>
<td>TBD</td>
<td>TBD Microfluidics Theory</td>
<td></td>
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<tr>
<td></td>
<td>TBD</td>
<td>TBD Design and Simulation of LoC devices</td>
<td></td>
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</tbody>
</table>
### Important Dates and Deadlines

Students are encouraged to refer to the academic calendar often to be aware of critical deadlines throughout the semester. The academic calendar can be found at [www.registrar.wsu.edu/Registrar/Apps/AcadCal.ASPX](http://www.registrar.wsu.edu/Registrar/Apps/AcadCal.ASPX). Questions regarding the academic calendar can be directed to the Office of Student Affairs in VSSC 100 or call 360-546-9559.

<table>
<thead>
<tr>
<th>Week</th>
<th>Topic</th>
<th>HW</th>
<th>Notes</th>
</tr>
</thead>
<tbody>
<tr>
<td>5</td>
<td>TBD</td>
<td></td>
<td>Fabrication of LoC Devices</td>
</tr>
<tr>
<td></td>
<td>TBD</td>
<td></td>
<td>Soft Lithography</td>
</tr>
<tr>
<td>6</td>
<td>TBD</td>
<td></td>
<td>Materials choice for LoC devices</td>
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<tr>
<td></td>
<td>TBD</td>
<td></td>
<td>Device packaging</td>
</tr>
<tr>
<td>7</td>
<td>TBD</td>
<td></td>
<td>Mid-Term</td>
</tr>
<tr>
<td>8</td>
<td>TBD</td>
<td></td>
<td>Selection of sensors</td>
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<td></td>
<td>TBD</td>
<td></td>
<td>Integration with sensors</td>
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<tr>
<td>9</td>
<td>TBD</td>
<td></td>
<td>Integration with Digital Circuits</td>
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<tr>
<td></td>
<td>TBD</td>
<td></td>
<td>Choice of power systems</td>
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<tr>
<td>10</td>
<td>TBD</td>
<td></td>
<td>Spring Break</td>
</tr>
<tr>
<td>11</td>
<td>TBD</td>
<td></td>
<td>Current mode circuits</td>
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<tr>
<td></td>
<td>TBD</td>
<td></td>
<td>Impedance mode circuits</td>
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<tr>
<td>12</td>
<td>TBD</td>
<td></td>
<td>Paper-based Diagnostics</td>
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<tr>
<td></td>
<td>TBD</td>
<td></td>
<td>Case study 1: Electrochemical Biosensor</td>
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<tr>
<td>13</td>
<td>TBD</td>
<td></td>
<td>Case study 2: Cell Manipulation</td>
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<td></td>
<td>TBD</td>
<td></td>
<td>Case study 3: Nanowire Sensors</td>
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<tr>
<td>14</td>
<td>TBD</td>
<td></td>
<td>Case study 4: DNA Analysis</td>
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<td></td>
<td>TBD</td>
<td></td>
<td>Case study 5: Environmental Monitoring</td>
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<tr>
<td>15</td>
<td>TBD</td>
<td></td>
<td>Case study 6: Gas Sensor</td>
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<td></td>
<td>TBD</td>
<td></td>
<td>Review</td>
</tr>
<tr>
<td>16</td>
<td>TBD</td>
<td></td>
<td>Finals (Closed Book)</td>
</tr>
</tbody>
</table>